

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE
(Case No. RA043D2DC)

#6/A
LDS
9-8-00
entered

In the Application of:)
|)
| FARMWALD ET AL.) Group
|) Art Unit:
Serial No: CONTINUATION OF 09/161,090)
|) Before
Filed: Herewith) Examiner:
|)
Title: INTEGRATED CIRCUIT HAVING MEMORY)
| WHICH SYNCHRONOUSLY SAMPLES)
| INFORMATION WITH RESPECT TO)
| EXTERNAL CLOCK SIGNALS)

Assistant Commissioner for Patents
Washington, DC 20231

PRELIMINARY AMENDMENT

Dear Sir:

Prior to the examination of the above-referenced application,
kindly amend the application as follows:

IN THE ABSTRACT:

Please delete the Abstract of the Disclosure and substitute
the attached Abstract of the Disclosure.

IN THE TITLE:

Please delete the title and substitute --SYSTEM HAVING DOUBLE
DATA TRANSFER RATE AND INTEGRATED CIRCUIT THEREFOR--.

A